

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant: Stewart S. Taylor et al.

Title: DISTORTION MANAGEMENT APPARATUS, SYSTEMS, AND METHODS

Docket No.: 884.B91US1

Filed: February 25, 2004

Examiner: Unknown

Serial No.: 10/786677

Due Date: N/A

Group Art Unit: 2817



**Mail Stop Amendment**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

A return postcard.  
 An Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of 4 cited documents.

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

By: Ann M. McCrackin

Atty: Ann M. McCrackin

Reg. No. 42,858

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 31 day of August, 2004.

Name

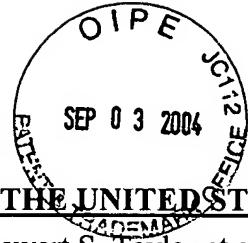
Kacia Lee

Signature

Kacia Lee

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

(GENERAL)



S/N 10/786677

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Stewart S. Taylor et al.	Examiner:	Unknown
Serial No.:	10/786677	Group Art Unit:	2817
Filed:	February 25, 2004	Docket:	884.B91US1
Title:	DISTORTION MANAGEMENT APPARATUS, SYSTEMS, AND METHODS		
Assignee:	Intel Corporation	Customer No:	21186

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**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

INFORMATION DISCLOSURE STATEMENT

Serial No : 10/786677

Filing Date: February 25, 2004

Title: DISTORTION MANAGEMENT APPARATUS, SYSTEMS, AND METHODS

Assignee: Intel Corporation

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Dkt: 884.B91US1 (INTEL)

The present application is either a U.S. national patent application filed after June 30, 2003 or an international application that entered the national stage under 35 U.S.C. § 371 after June 30, 2003. Thus, Applicant believes that the U.S. Patent & Trademark Office has waived the requirement under 37 C.F.R. 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication. The waiver is provided in a pre-OG notice from the U.S. Patent & Trademark Office entitled "Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003" and dated July 11, 2003. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

STEWART S. TAYLOR ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
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Date Aug. 30, 2004

By Ann M. McCrackin

Ann M. McCrackin

Reg. No. 42,858

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KACIA LEE  
Name

Kacia Lee  
Signature

Substitute for form 1449A/PTO		Complete if Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT E</b> (Use as many sheets as necessary)		<b>Application Number</b>	10/786677
		<b>Filing Date</b>	February 25, 2004
		<b>First Named Inventor</b>	Taylor, Stewart
		<b>Group Art Unit</b>	2817
		<b>Examiner Name</b>	Unknown
Sheet 1 of 1		Attorney Docket No: 884.B91US1	

**US PATENT DOCUMENTS**

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>
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**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		GILBERT, BARRIE , "Analog at Milepost 2000: A Personal Perspective", Proceedings of the IEEE, Vol. 89, No. 3, (March 2001), 289-304	
		GILBERT, B , "Circuits for the precise synthesis of the sine function", Electronics Letters, 13(17), (August 18, 1977), 506-8	
		GILBERT, B , "Translinear circuits: a proposed classification", Electronics Letters, 11(1), (January 1975), 14-16	
		GILBERT, B , "Translinear circuits: an historical overview", Analog Integrated Circuits and Signal Processing, 9(2), (March 1996), 95-118	

**EXAMINER****DATE CONSIDERED**